

FIG. 1

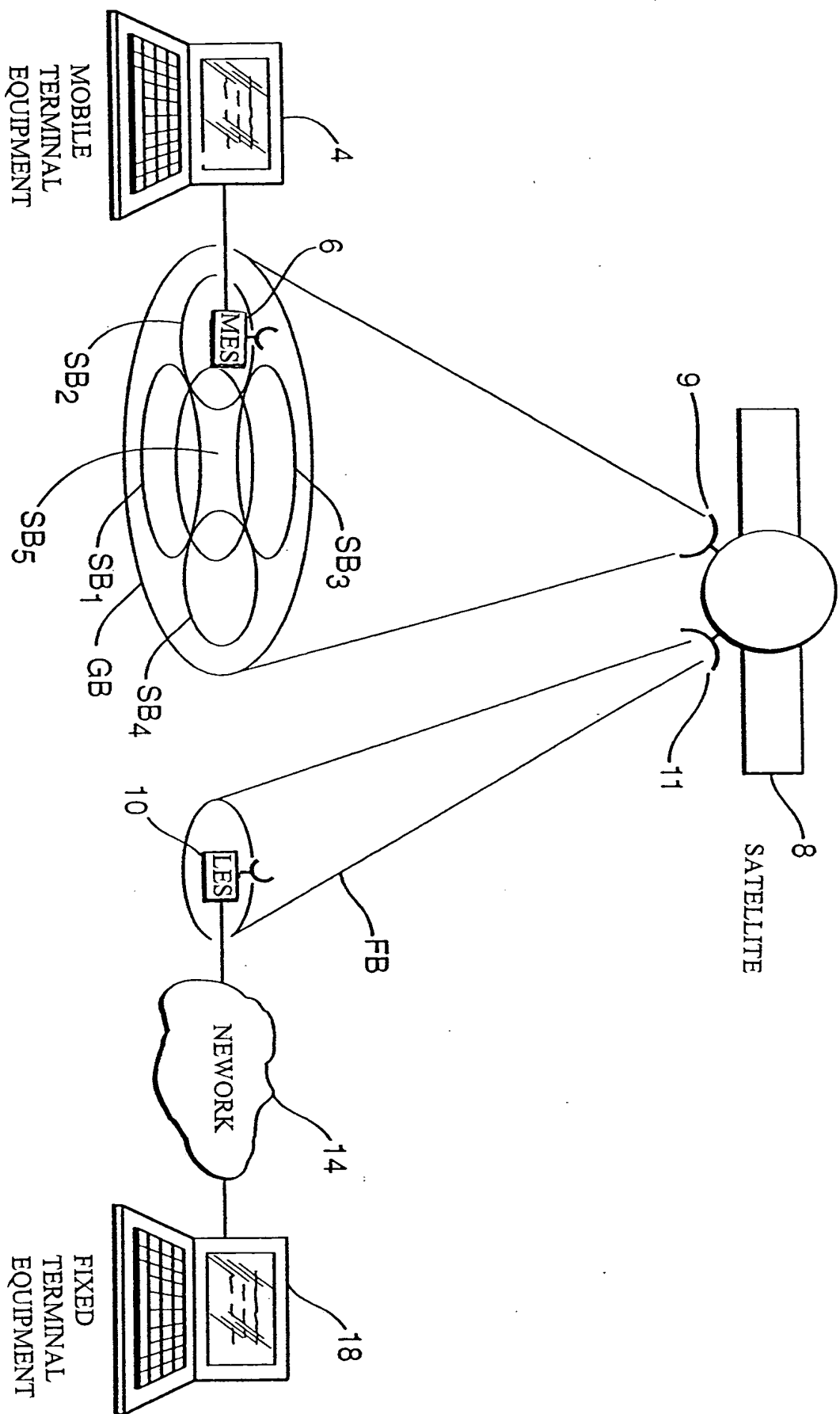


FIG. 2a

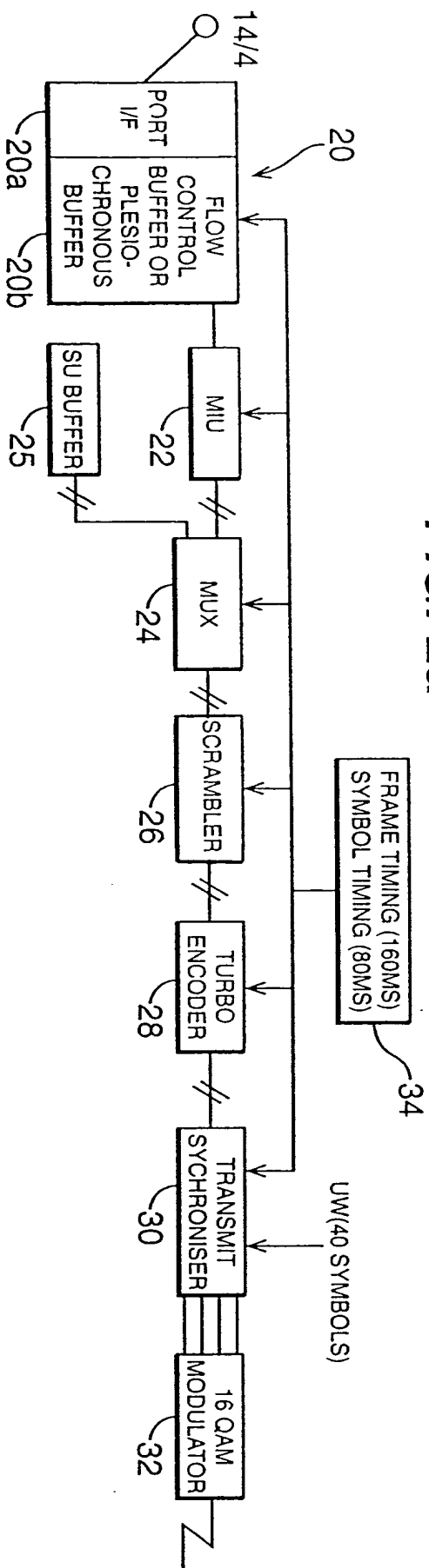


FIG. 2b

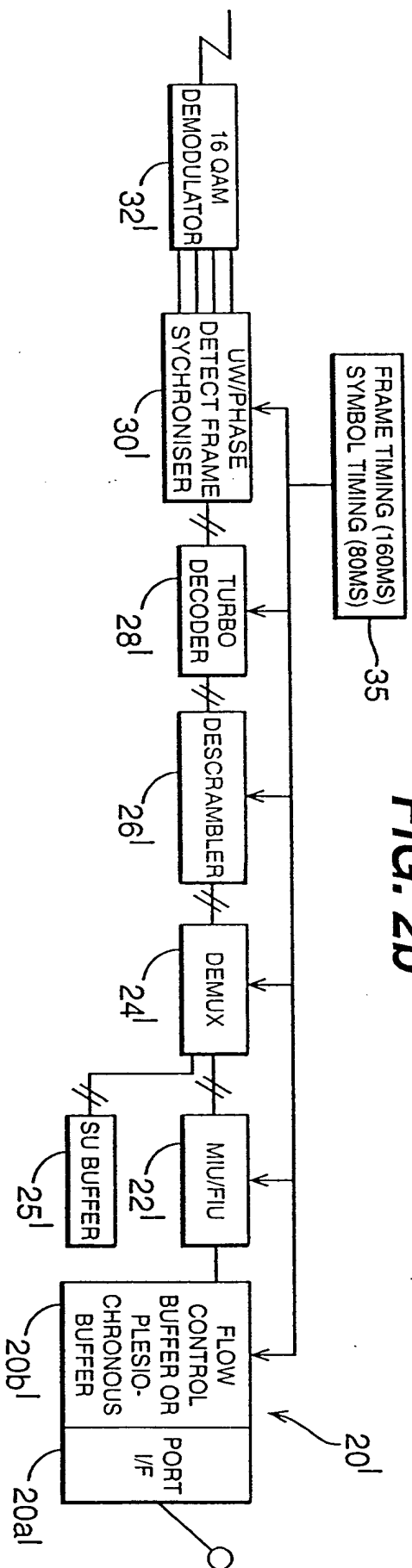


FIG. 3

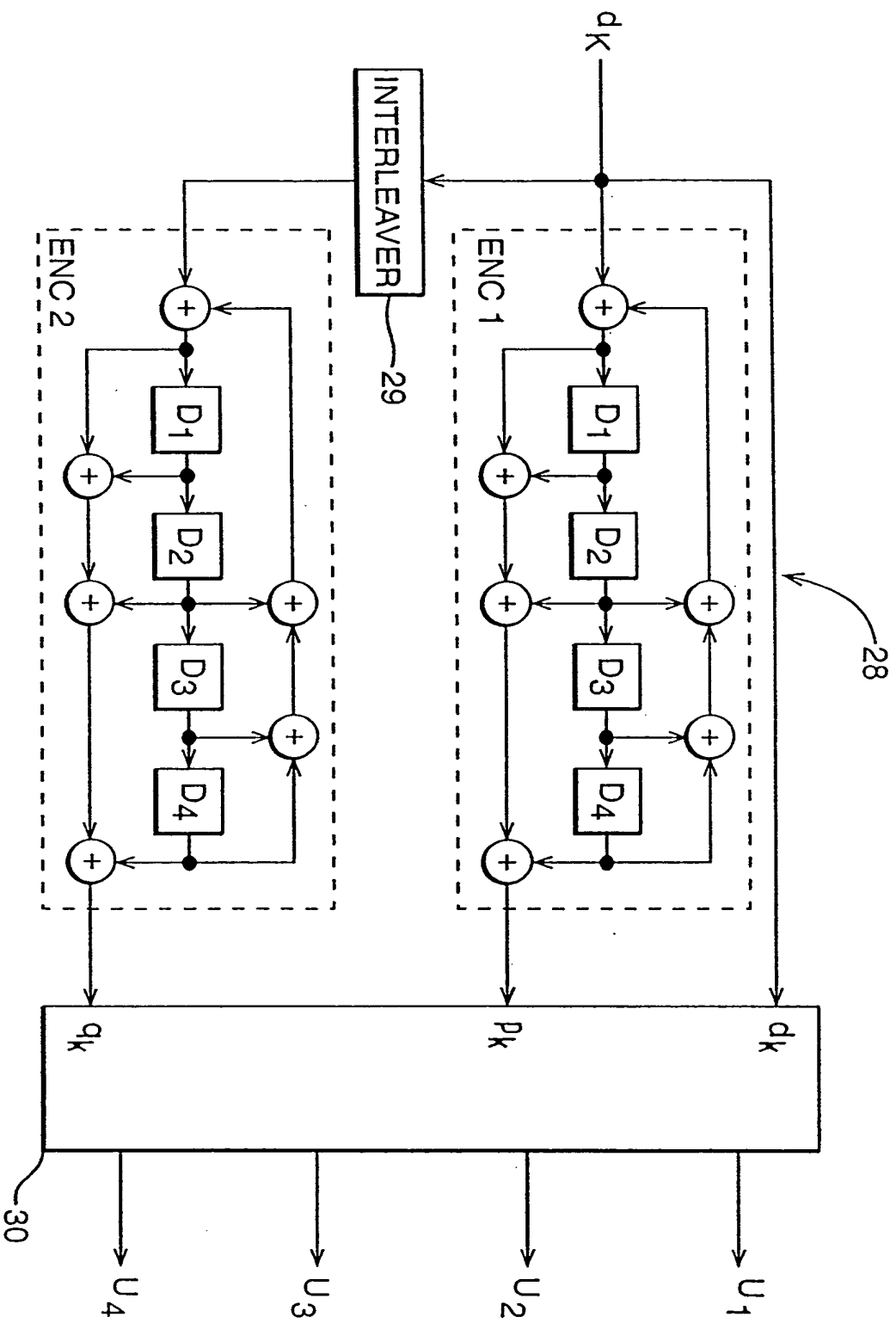


FIG. 4

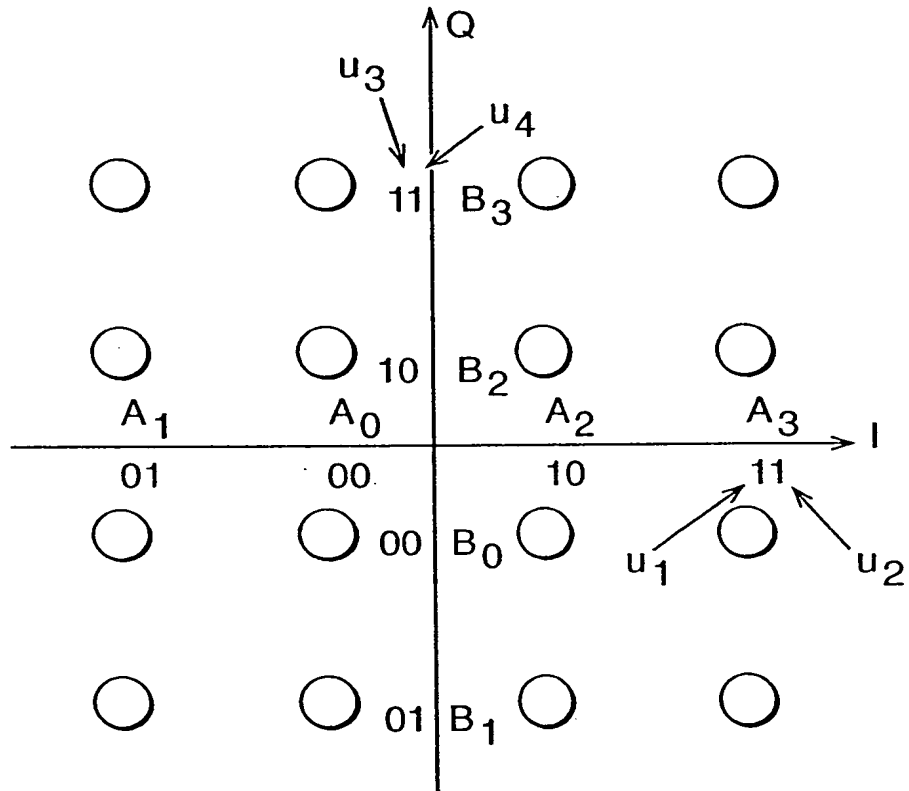
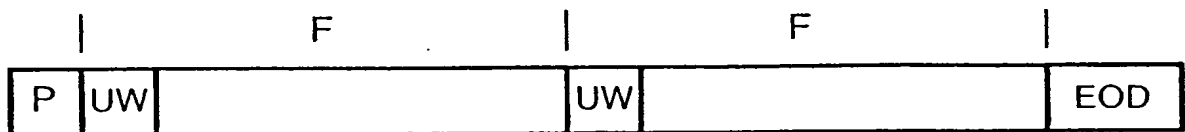


FIG.17



Timing diagram of a 160ms frame. The frame contains 1280 symbols. Pilot symbols are located at positions #1, #2, #46, #47, and #48. A UW (Unwired) symbol is at the beginning, and a DS (Data Symbol) is indicated. The frame is divided into sections of 251 symbols each, with a total duration of 160ms.

Timing diagram for SF1 and SF2 symbols. The diagram shows two horizontal timelines. The top timeline is labeled 'SF1' and has a total duration of 251 units, with an 80ms segment marked. The bottom timeline is labeled 'SF2' and also has a total duration of 251 units, with an 80ms segment marked. A vertical line indicates a 160ms interval between the start of the two timelines.

The diagram illustrates the timing of the proposed scheme. It shows a sequence of four data segments, each 592 bits long, labeled M_1 , M_2 , M_1 , and M_2 . The segments are separated by 40ms intervals, and the total duration is 160ms.

FIG. 7a

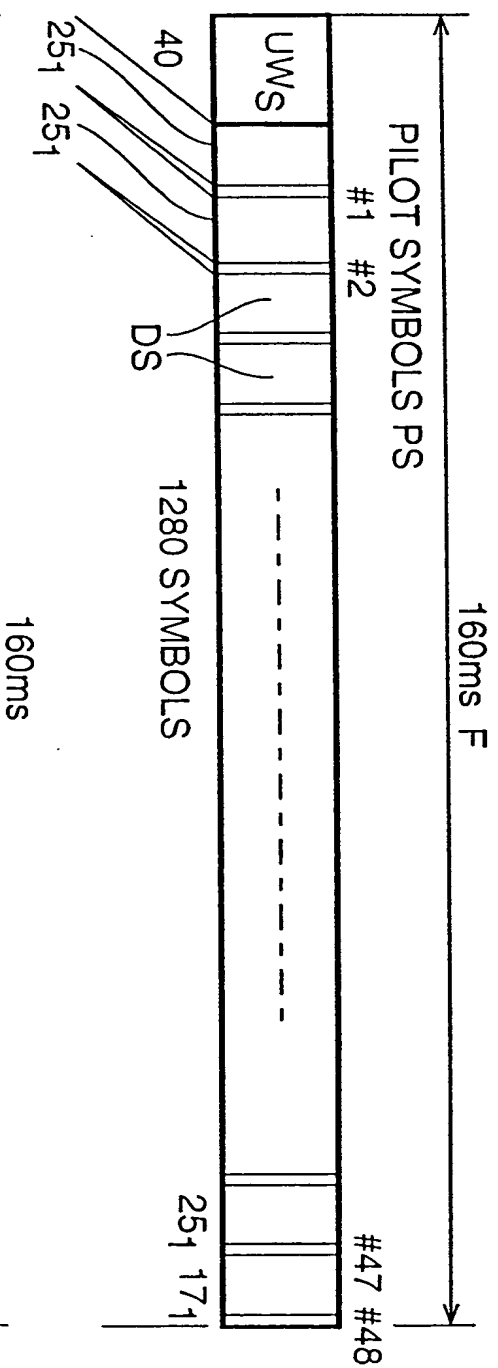


FIG. 7b

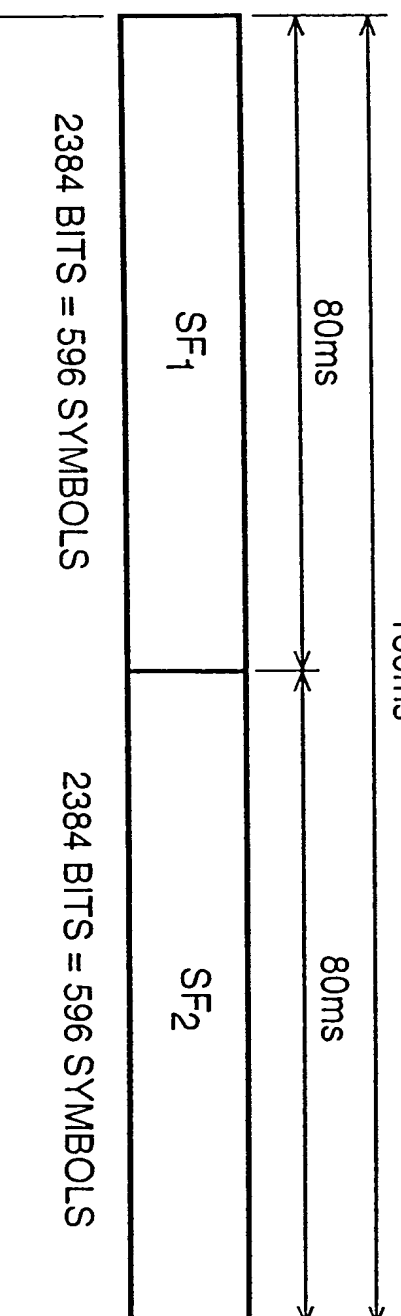
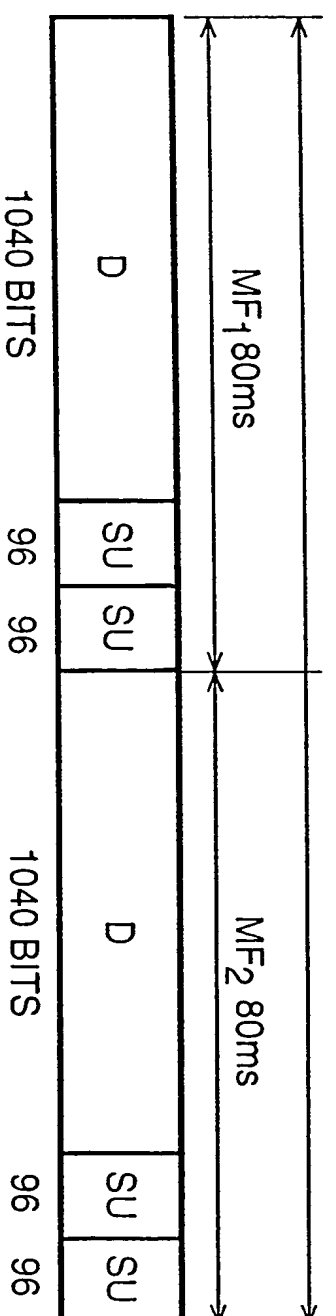


FIG. 7c



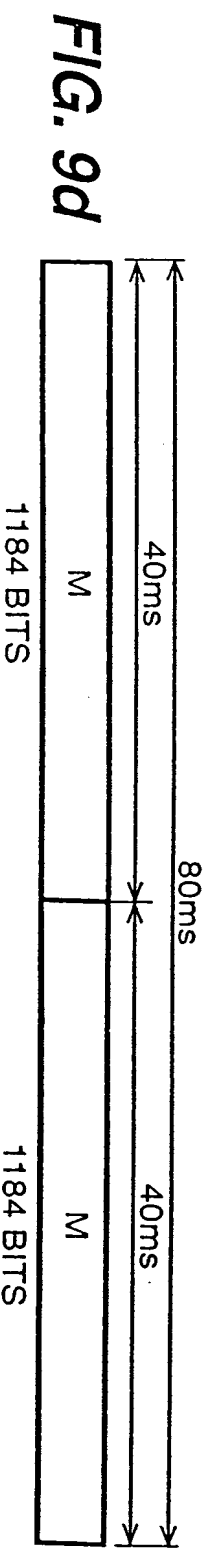
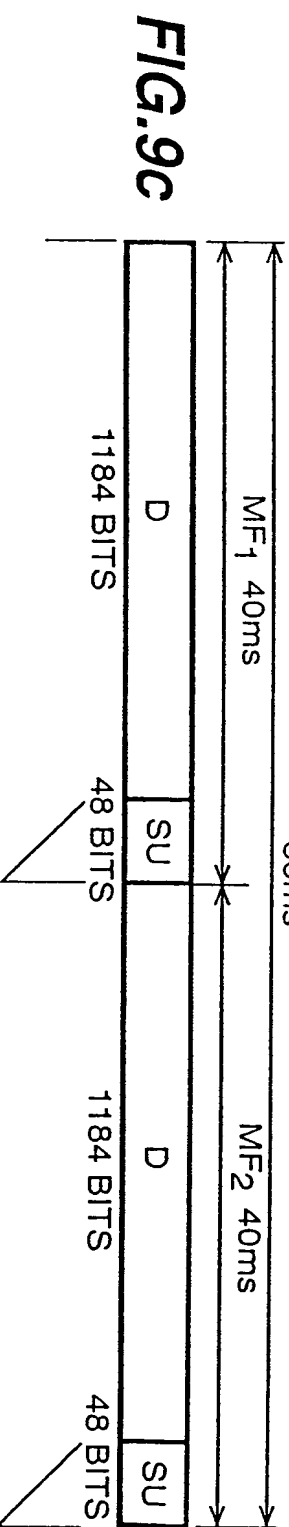
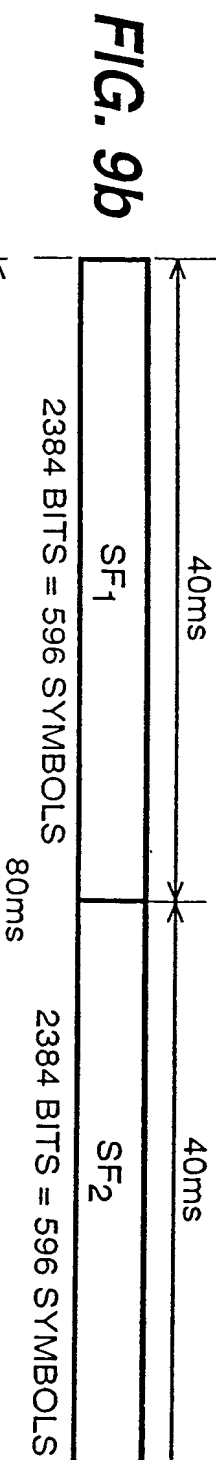
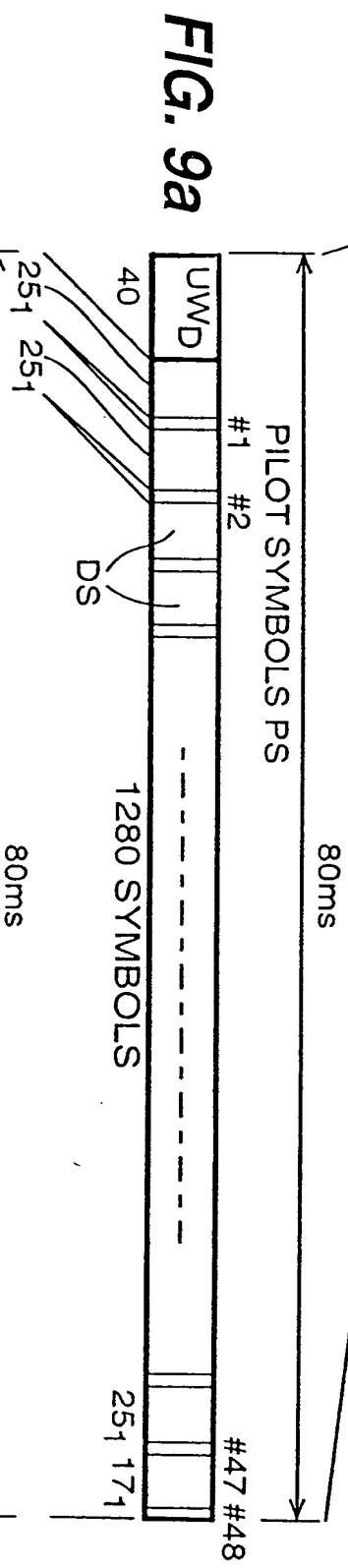
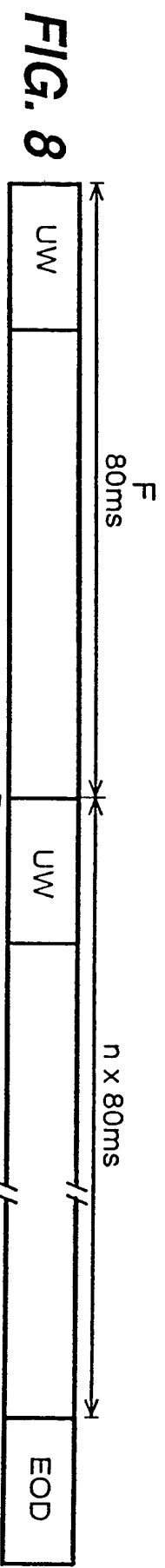


FIG. 10a

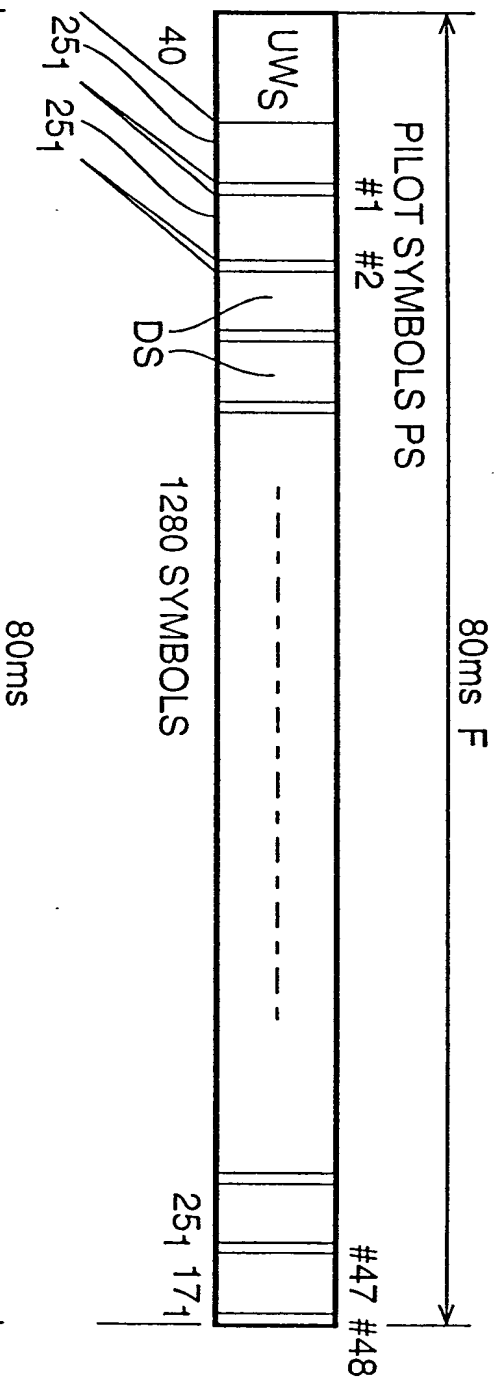


FIG. 10b

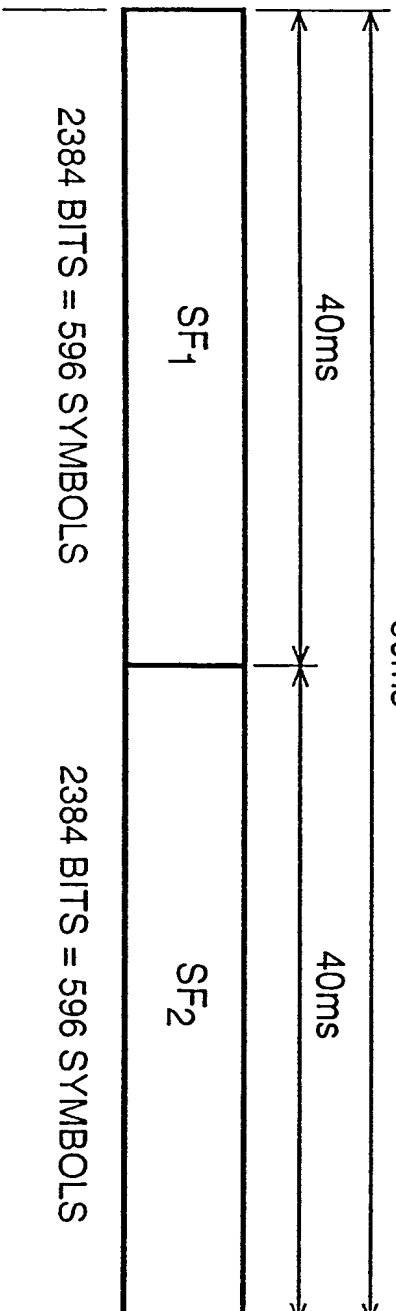


FIG. 10c

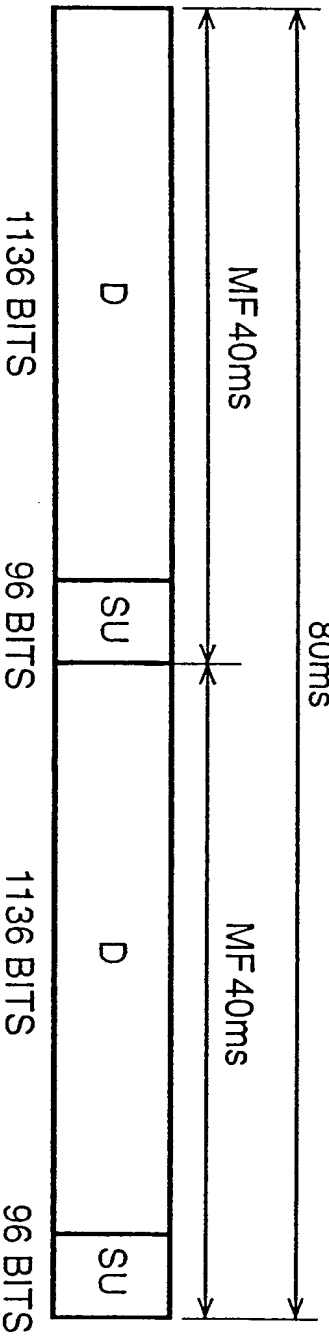


FIG. 11

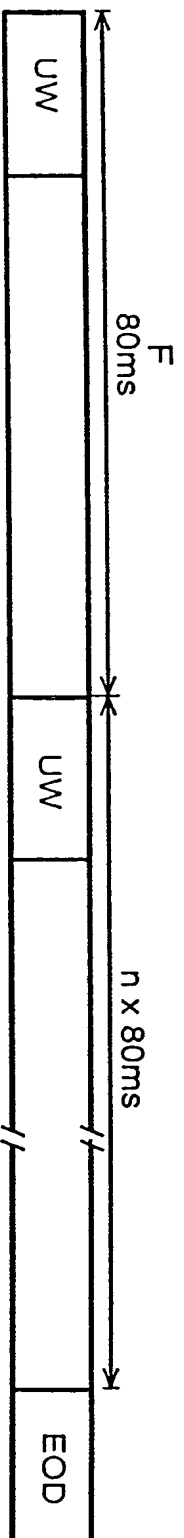


FIG. 12a

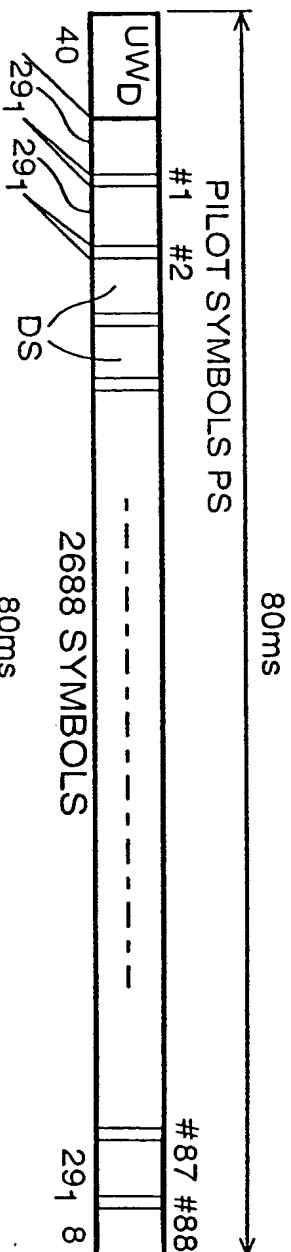


FIG. 12b

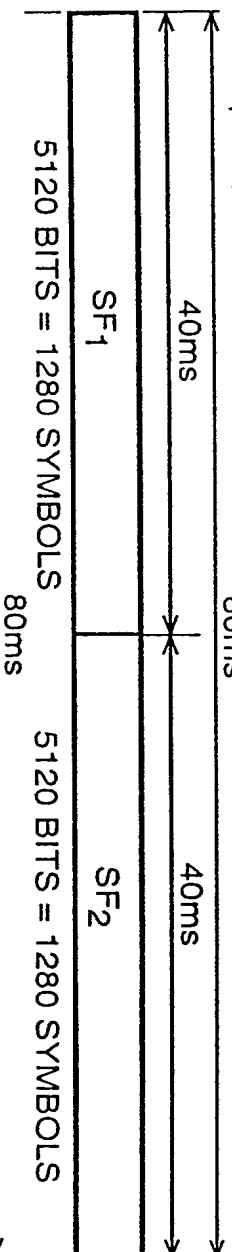


FIG. 12C

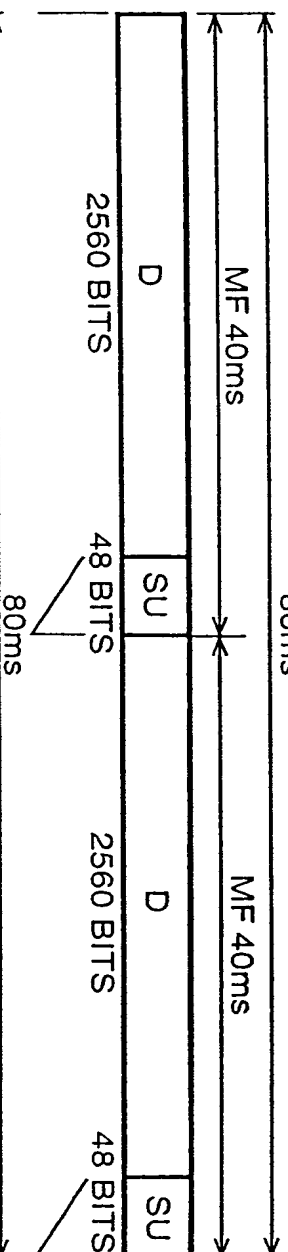


FIG. 12d

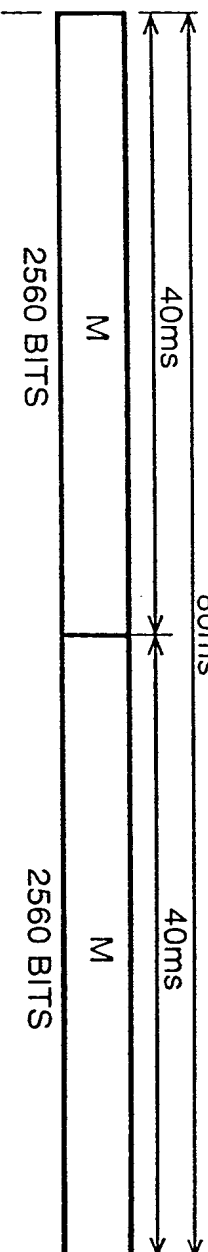


FIG. 12e

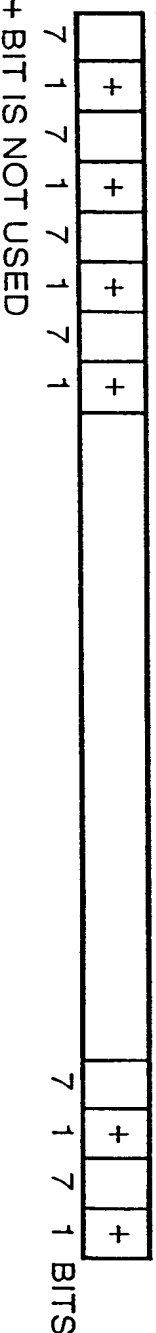


FIG. 13a

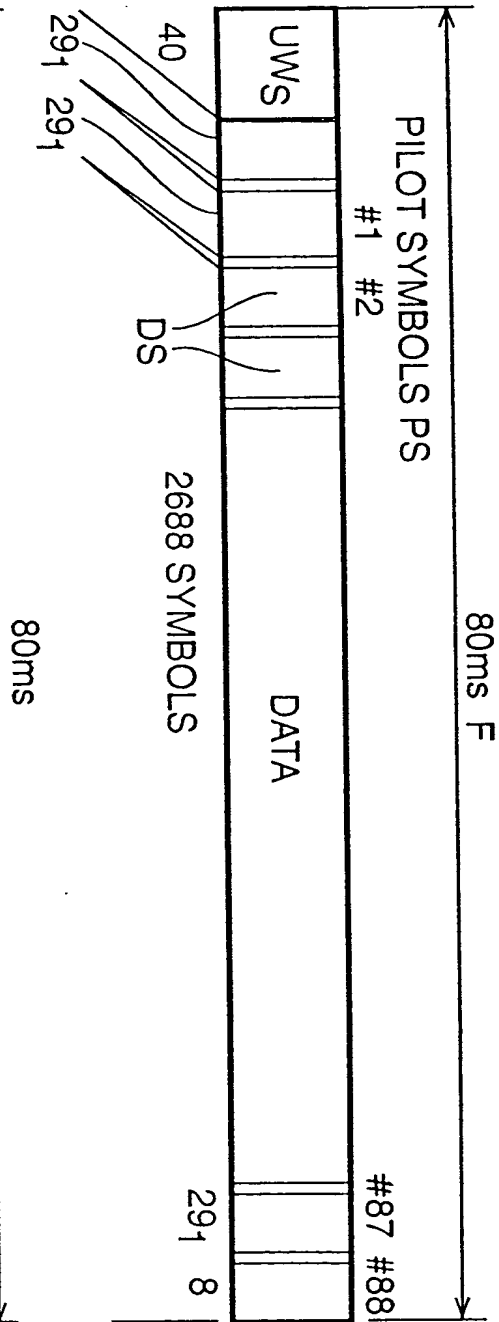


FIG. 13b

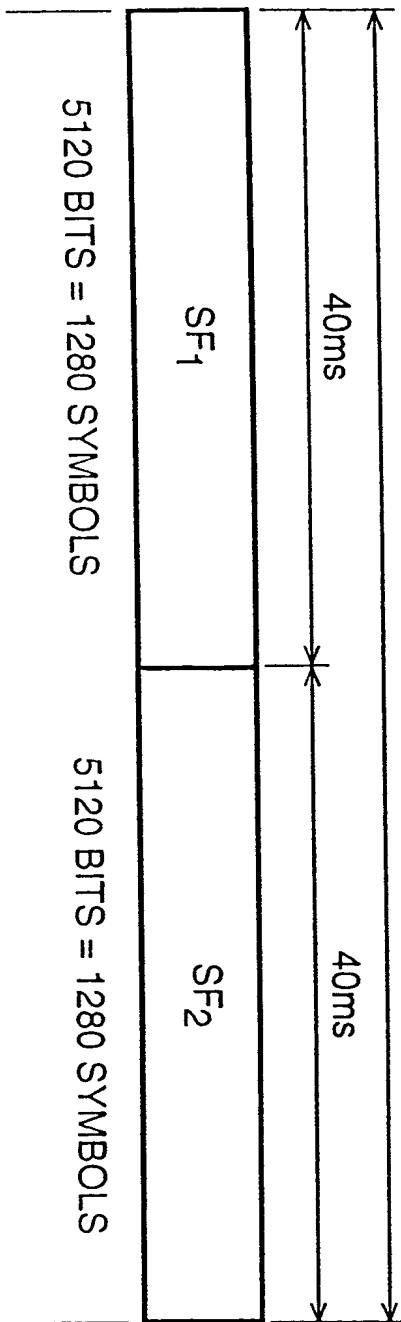
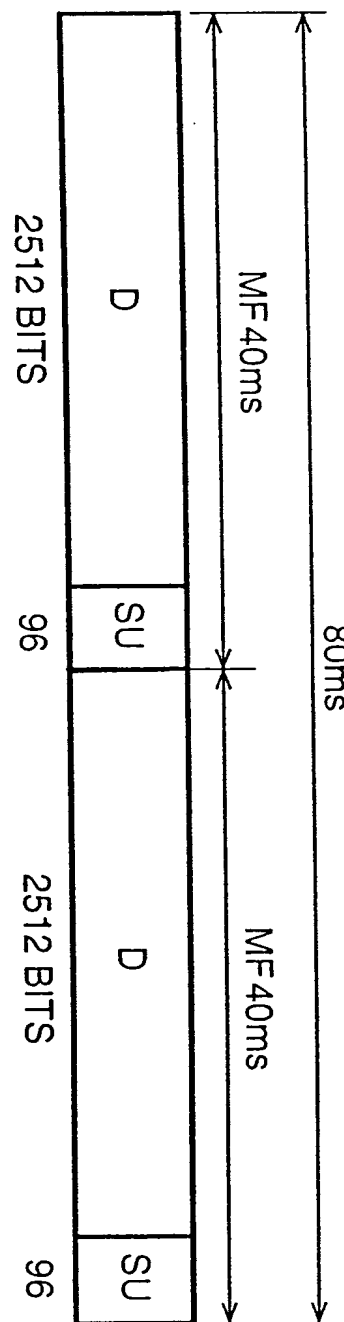


FIG. 13c



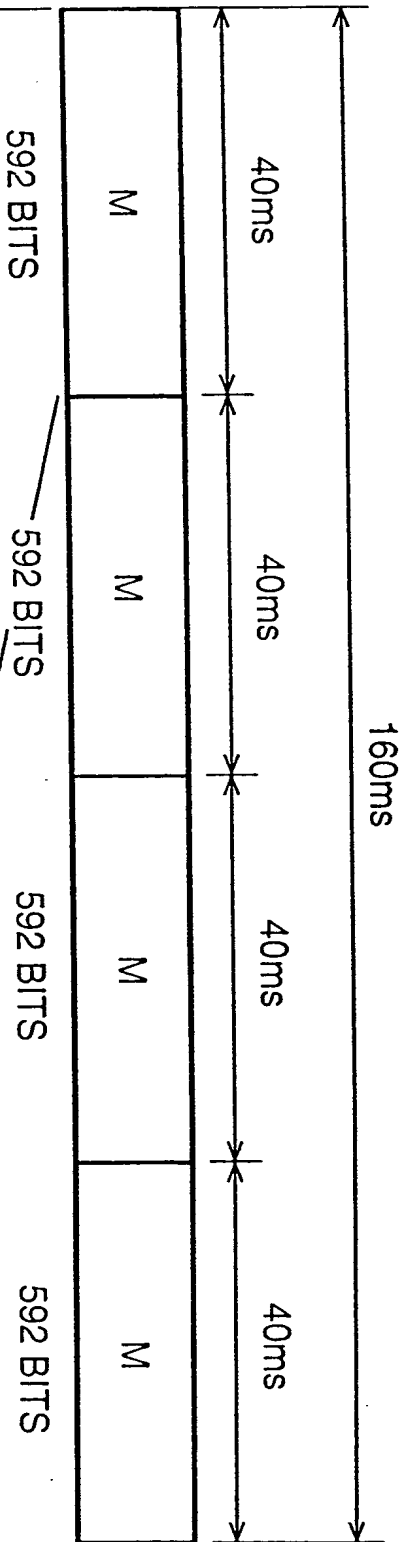


FIG. 14a

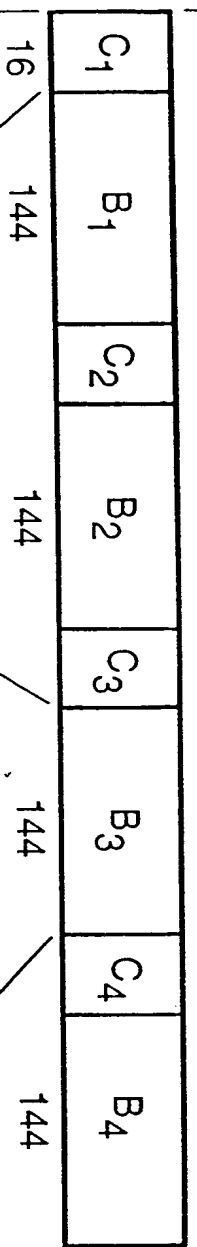


FIG. 14b

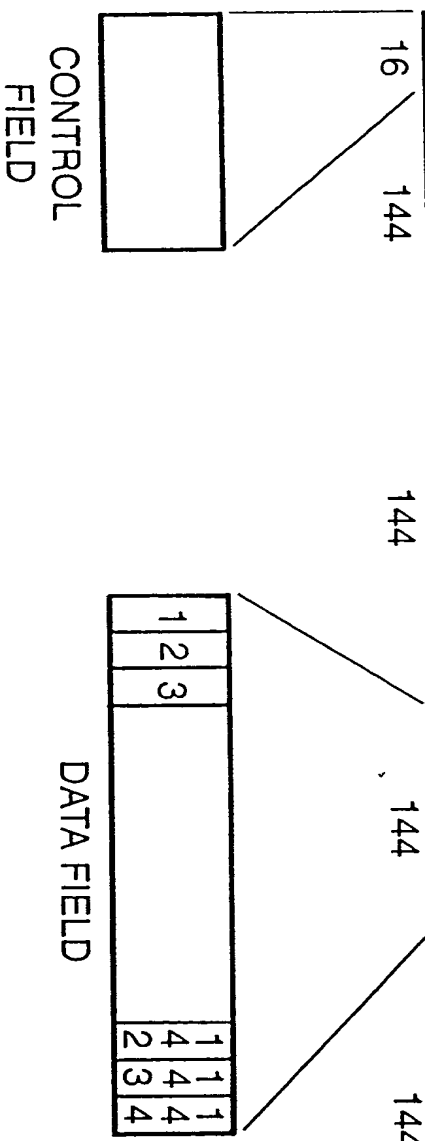


FIG. 14c

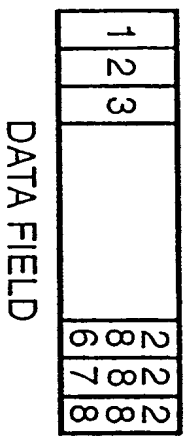
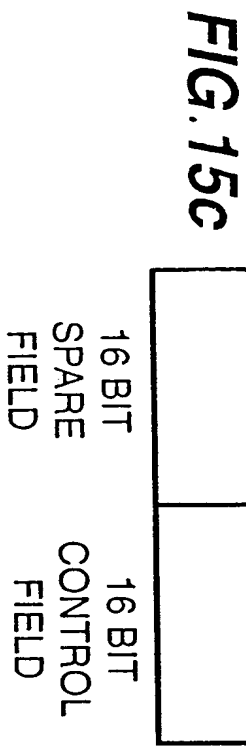
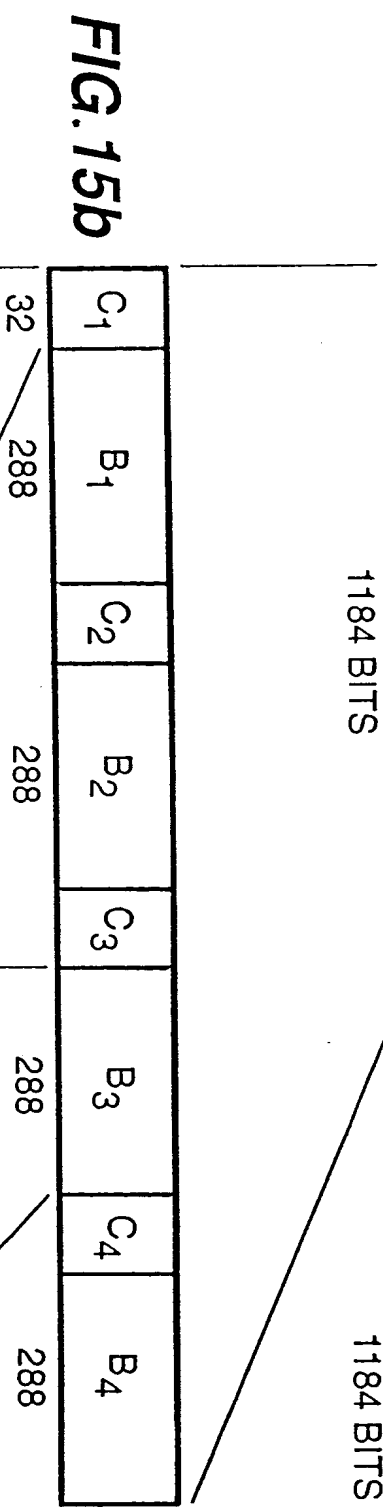
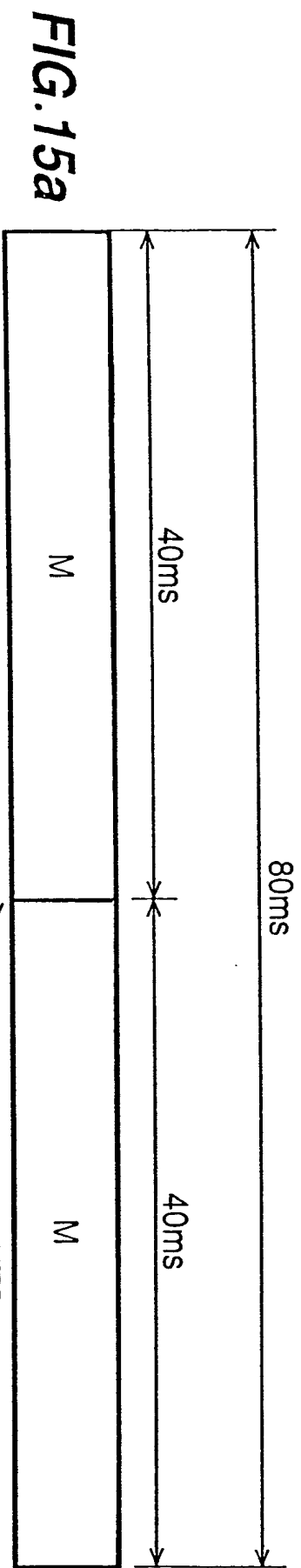


FIG. 16

